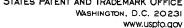


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Bib Data Sheet

CONFIRMATION NO. 9164

SERIAL NUMBER 09/765,958	FILING DATE 01/18/2001 RULE	CLASS -702- 7 (4	2857	ROUP ART UNIT -2857- 2133		ATTORNEY DOCKET NO. 260/085 US	
	oglu, Mountain View, CA Cooke, Los Gatos, CA;	;					
** CONTINUING DATA **********************************							
** FOREIGN APPLICATIONS ************************************							
Foreign Priority claimed 35 USC 119 (a-d) condition met Verified and Acknowledged	er STATE OR COUNTRY CA	SHEETS TOT DRAWING CLA 12 14		MS	INDEPENDENT CLAIMS 3		
ADDRESS 22249 # 23639							
TITLE Hierarchical test circuit structure for chips with multiple circuit blocks							
RECEIVED No.	ES: Authority has been given in Paper to charge/credit DEPOSIT ACCOUNT for following:			All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue) Other Credit			